

**AMENDMENTS TO THE SPECIFICATION**

**Please replace the first paragraph on page 6 of the originally filed specification with the following amended paragraph:**

Figure 4 is a diagram for understanding the operation of the ~~digital-to-analog~~analog-to-digital converter of the phase control circuit according to Figure 2.

**Please replace the first full paragraph on page 9 of the originally filed specification with the following amended paragraph:**

Figure 4 shows the mode of operation of the ~~digital-to-analog~~analog-to-digital converter 22, which acts here as a window comparator which provides a high or low signal when the input lies outside of the window. The structure of a window comparator with a given characteristic is familiar to the person skilled in the art. The mode of operation is such that a “0” is emitted if the input voltage lies in a narrow range around the setpoint value. If it lies above, a “1” is emitted while if it lies below a “-1” is emitted.